

APPLICATION DATA SHEET

APPLICATION INFORMATION

Filing Date:: 02-26-04
Application Type:: Regular
Subject Matter:: Utility
Title:: Structures and Methods for
Determining the Effects of High
Stress Currents on Conducting
Layers and Contacts in Integrated
Circuits
Attorney Docket Number:: X-1010-2D US
Request for Early Pub?:: No
Request for Non-Pub?:: Yes
Total Drawing Sheets:: 06
Small Entity?:: No
Petition included?:: No

APPLICANT INFORMATION

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: Netherlands
Status:: Full Capacity
Given Name:: Jan L.
Family Name:: de Jong
Street:: 19871 Rodrigues Avenue
City:: Cupertino
State or Province:: California
Postal or Zip Code:: 95014

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: US
Status:: Full Capacity
Given Name:: Zicheng G.
Family Name:: Ling
Street:: 5505 Manderston Drive

City:: San Jose
State or Province:: California
Postal or Zip Code:: 95138

CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 24309

REPRESENTATIVE INFORMATION

Representative Customer Number::	24309	
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DOMESTIC PRIORITY INFORMATION

Application::	Continuity Type::	Parent Application::	Parent Filing Date::
This Applic.	Divisional	10/109,744	03-28-02

ASSIGNEE INFORMATION

Assignee Name:: Xilinx, Inc.
Street:: 2100 Logic Drive
City:: San Jose
State or Province:: California
Postal or Zip Code:: 95124